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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/690,275	10/22/2003	Yasuji Yamasaki	117145	4581	
25944	7590 06/05/2006		EXAM	EXAMINER	
OLIFF & BERRIDGE, PLC			VU, HUNG K		
P.O. BOX 19928 ALEXANDRIA, VA 22320			ART UNIT	PAPER NUMBER	
	,		2811		
			DATE MAILED: 06/05/2000	DATE MAILED: 06/05/2006	

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)				
Office Action Commence	10/690,275	YAMASAKI ET AL.				
Office Action Summary	Examiner	Art Unit				
	Hung Vu	2811				
The MAILING DATE of this communication appears on the cover sheet with the correspondence address Period for Reply						
A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication. - If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication. - Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).						
Status						
1)⊠ Responsive to communication(s) filed on <u>16 March 2006</u> .						
<u> </u>						
3) Since this application is in condition for allowan	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is					
closed in accordance with the practice under Ex parte Quayle, 1935 C.D. 11, 453 O.G. 213.						
Disposition of Claims						
4) Claim(s) 2-22 is/are pending in the application.						
4a) Of the above claim(s) is/are withdrawn from consideration.						
5)⊠ Claim(s) <u>19-21</u> is/are allowed. ´						
6)⊠ Claim(s) <u>2-6,15-18 and 22</u> is/are rejected.						
7) Claim(s) <u>7-14</u> is/are objected to.						
8) Claim(s) are subject to restriction and/or election requirement.						
Application Papers						
9) The specification is objected to by the Examiner.						
10) The drawing(s) filed on is/are: a) accepted or b) objected to by the Examiner.						
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).						
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).						
11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.						
Priority under 35 U.S.C. § 119						
12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) All b) Some * c) None of:						
1. Certified copies of the priority documents have been received.						
2. Certified copies of the priority documents have been received in Application No						
3. Copies of the certified copies of the priority documents have been received in this National Stage						
application from the International Bureau (PCT Rule 17.2(a)). * See the attached detailed Office action for a list of the certified copies not received.						
See the attached detailed Office action for a list of	or the certified copies not receive	u.				
	·					
Attachment(s)	_	•				
1) Notice of References Cited (PTO-892) 4) Interview Summary (PTO-413) Paper No(s)/Mail Date						
Notice of Draftsperson's Patent Drawing Review (PTO-948) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date		atent Application (PTO-152)				

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DETAILED ACTION

Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 2, 3, 5, 6, 15, 16, 18 and 22 are rejected under 35 U.S.C. 102(b) as being anticipated by Kurashina (US 2002/0060756, of record).

Kurashina discloses, as shown in Figure 7, an electro-optical device, comprising:

data lines (6A) extending in a first direction above a substrate (10);

scanning lines (3a) extending in a second direction and intersecting with the data lines;

pixel electrodes (9a) and thin film transistors disposed corresponding to intersections of the data lines and the scanning lines;

storage capacitors (70-3) electrically connected to the thin film transistors and the pixel electrodes, and

a light-shielding film (420) disposed between the data lines and the pixel electrodes, one of a pair of electrodes forming the storage capacitor being formed of a multi-layered film containing a low resistance film (see sections [0044] and [0152]).

Regarding claim 2, Kurashina discloses the multi-layered film being formed of a light-absorbing film (polysilicon or polysilicide) as a bottom layer and a light-reflecting film as a top layer (aluminum, titanium, tatalum, tungsten).

Regarding claim 3, Kurashina discloses one of the pair of electrodes forming each of the storage capacitors forms part of a capacitor line formed in the second direction; and

the capacitor line being formed of a multi-layered film containing a low resistance film.

Regarding claim 5, Kurashina discloses the low resistance film being made of aluminum [0019].

Regarding claim 6, Kurashina discloses each of the thin film transistors comprising:

a semiconductor layer including a channel region extending in the longitudinal direction and channel adjacent regions extending in the longitudinal direction further from the channel region, and a light-shielding portion is provided at both sides of the channel region [Figure 7].

Claim Rejections - 35 USC § 103

- 2. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
 - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

Claims 4 and 14 are rejected under 35 U.S.C. 103(a) as being unpatentable over Kurashina (US 2002/0060756, of record).

Kurashina discloses all of the claimed limitations except the multi-layered film and the data lines as the same film. However, it would have been obvious to one of ordinary skill in the art at the time the invention was made to form the multi-layered film and the data lines of Kurashina as the same film as that claimed by Applicant, since it has been held to be within the general skill of a worker in the art to select a known material on the basis of its suitability for the intended use as a matter of obvious design choice. *In re Leshin*, 125 USPQ 416.

Allowable Subject Matter

- 3. Claims 19-21 are allowed.
- 4. Claims 7-14 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

Response to Arguments

5. Applicant's arguments filed 03/16/06 have been fully considered but they are not persuasive.

It is argued, at pages 10-11 of the Remarks, that Kurashina does not disclose one of a pair of electrodes forming the storage capacitor being formed of a multi-layered film containing a low resistance film. This argument is not convincing because Kurashina discloses, as shown in sections [0044] and [0152], that one of a pair of electrodes forming the storage capacitor being formed of a multi-layered film containing a low resistance film.

Conclusion

6. THIS ACTION IS MADE FINAL. Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Hung Vu whose telephone number is (571) 272-1666. The examiner can normally be reached on Tuesday to Friday 6:00-4:30.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Eddie C. Lee can be reached on (571) 272 - 1732. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Vu

May 17, 2006

Hung Vu

Primary Examiner